

2STL1360 2STX1360

Low voltage fast-switching NPN power transistors

Features

- Very low collector-emitter saturation voltage
- High current gain characteristic
- Fast-switching speed

Applications

- Emergency lighting
- LED
- Voltage regulation
- Relay drive

Description

The devices are NPN transistors manufactured using new "PB-HCD" (power bipolar high current density) technology. The resulting transistor shows exceptional high gain performances coupled with very low saturation voltage.

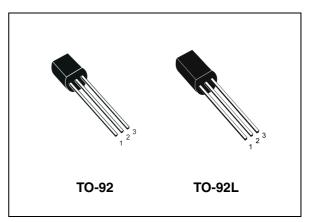
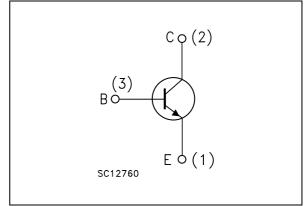


Figure 1. Internal schematic diagram



Order codes	Marking	Packages	Packaging
2STL1360	L1360	TO-92L	Bag
2STX1360	X1360	TO-92	Bag

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1 Electrical ratings

Symbol	Parameter	Value		Unit
	Farameter	2STX1360	2STL1360	Unit
V _{CBO}	Collector-base voltage (I _E = 0)	80		V
V _{CEO}	Collector-emitter voltage ($I_B = 0$)	6	0	V
V _{EBO}	Emitter-base voltage (I _C = 0)	6	3	V
Ι _C	Collector current	3		А
I _{CM}	Collector peak current (t _P < 5 ms) 5		А	
Ι _Β	Base current 0.2		А	
I _{BM}	Base peak current (t _P < 5 ms) 0.4		А	
P _{TOT}	Total dissipation at $T_{amb} = 25 \degree C$ 1 1.2		1.2	W
T _{STG}	Storage temperature	-65 to 150		°C
TJ	Max. operating junction temperature 150		50	°C

Table 3.Thermal data

Symbol	Parameter	Val	Unit		
Symbol	Falanielei	TO-92	TO-92L	Onit	
R _{thJA}	Thermal resistance junction-ambient	max	125	104	°C/W

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2 Electrical characteristics

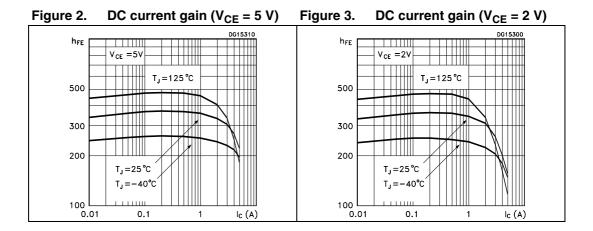
 T_{case} = 25 °C unless otherwise specified.

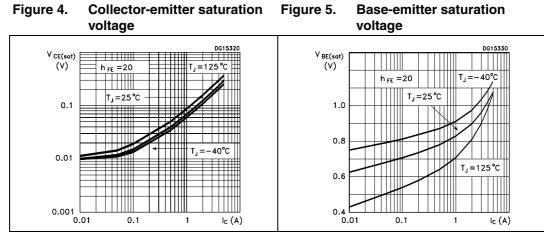
Symbol	Parameter	Test conditions		Min.	Тур.	Max.	Unit
I _{CBO}	Collector cut-off current $(I_E = 0)$	V _{CB} = 80 V				100	nA
I _{EBO}	Emitter cut-off current $(I_{\rm C} = 0)$	V _{EB} = 6 V				100	nA
V _{BE(on)}	Base-emitter on voltage	V _{CE} = 2 V	I _C = 100 mA	630	650	730	mV
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	I _C = 2 A I _C = 3 A	I _B = 100 mA I _B = 150 mA		130 180	300 500	mV mV
V _{BE(sat)} ⁽¹⁾	Base-emitter saturation voltage	I _C = 2 A	I _B = 100 mA		0.9	1.2	V
h _{FE} ⁽¹⁾	DC current gain	I _C = 0.1 A I _C = 1 A		80 160		400	
	RESISTIVE LOAD						
t _d	Delay time	$V_{CC} = 10 V$	I _C = 3 A		17	20	ns
t _r	Rise time	$I_{B(on)} = - I_{B(off)}$	_{f)} = 300 mA		81	100	ns
t _s	Storage time	$V_{BE(off)} = -5 V$			620	720	ns
t _f	Fall time				54	65	ns
f _T	Transition frequency	I _C = 0.1 A	V _{CE} = 10 V		130		MHz

 Table 4.
 Electrical characteristics

1. Pulse test: pulse duration \leq 300 µs, duty cycle \leq 2 %

2.1 Electrical characteristics (curves)







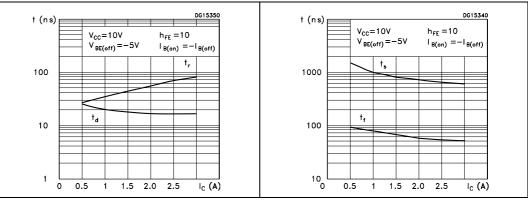
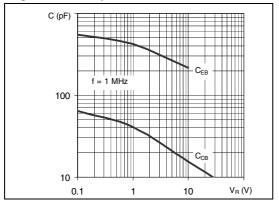


Figure 8. Capacitance





2.2 Test circuit

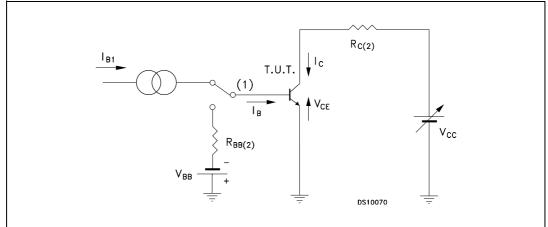


Figure 9. Resistive load switching test circuit

1. Fast electronic switch

2. Non-inductive resistor



3 Package mechanical data

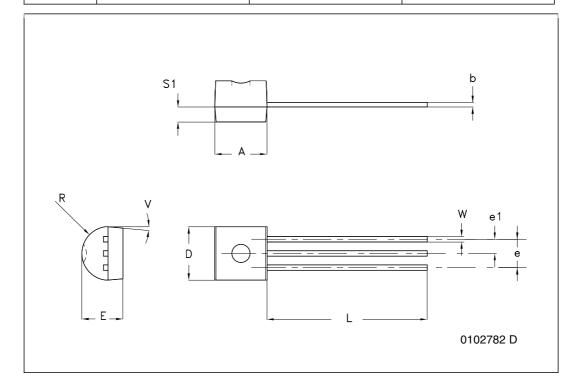
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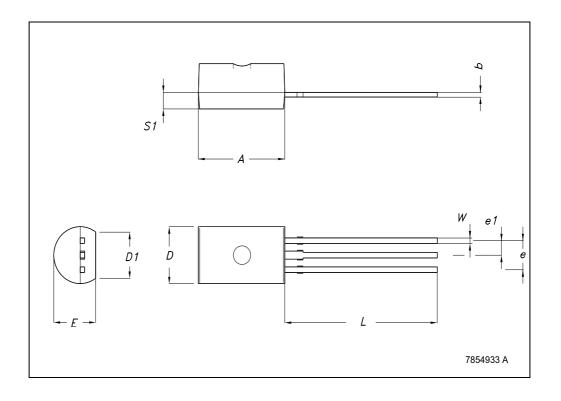
TO-92 bulk shipment mechanical data				
DIM.		mm.		
	MIN.	ТҮР	MAX.	
A	4.32		4.95	
b	0.36		0.51	
D	4.45		4.95	
E	3.30		3.94	
е	2.41		2.67	
e1	1.14		1.40	
L	12.70		15.49	
R	2.16		2.41	
S1	0.92		1.52	
w	0.41		0.56	
V		5 ⁰		





TO-92L MECHANICAL DATA

DIM.	mm.					
	MIN.	ТҮР	MAX.			
A	7.80		8.20			
b	0.35		0.45			
D	4.70		5.10			
D1		4				
E	3.70		4.10			
е	2.44		2.64			
e1		1.27				
L	13.30		14.30			
S1	1.28		1.58			
W	0.35		0.55			



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4 Revision history

Table 5.Document revision history

Date	Revision	Changes
20-Oct-2006	1	Initial release
16-Jul-2007	2	Added figures 2, 3, 4, 5, 6, 7 and 8
29-Oct-2009	3	Updated Figure 8 on page 4 and TO-92 package mechanical data



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